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For Immediate Release

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**CYPRESS ANNOUNCES SILICON ON BiCMOS PROCESS
WITH WORLD-CLASS SPEED, POWER CHARACTERISTICS**

Technology Will Support Penetration of Datacom, Telecom Market Segments

SAN JOSE, California...March 13, 2000 -- Cypress Semiconductor Corporation (NYSE:CY) today announced that it has obtained working silicon on a 0.25-micron BiCMOS, 3.3-volt, double-layer-metal process. The process will support new product development in next-generation communications products such as high-speed physical-layer devices, high-performance clock buffers, and wireless communications products for high-frequency RF applications.

The new BiCMOS process will provide an optimal mix of speed, power, and cost relative to competitive processes. Among the first products to be rolled out under the process are a high-performance transceiver for wide area networks, a high-speed programmable-skew clock buffer, and an RF phase locked loop that supports frequency outputs up to 2.5 GHz.

The new BiCMOS process supports transistor frequencies greater than 30 GHz and adds five masks to Cypress's highly cost-effective 0.25-micron CMOS process. The result is a leading bipolar process that maintains the density, power consumption, and versatility of 0.25-micron CMOS.

"This is a best-in-class BiCMOS process," said Paul Keswick, Cypress vice president of new product development. "Cypress aims to leverage it to create reusable IP blocks for future generations of high-performance communications products."

The company has developed broad competencies in technologies critical to communications product design, such as analog/mixed-signal phase-locked loops (PLLs), serializing/deserializing (SERDES) functionality, high-speed logic, and SRAM, FIFO and multiport memories. The compatibility of our BiCMOS process with Cypress's existing SRAM technology will facilitate the creation of targeted, integrated solutions for the communications market.

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"One of Cypress's goals in the communications business is to become the preferred supplier of high performance data path and routing solutions for Internet switching systems," said Dan McCranie, Cypress vice president of sales and marketing. "We are addressing high-speed telecom and networking systems that feed bandwidth-hungry Internet applications, such as IP routers and ATM switches, Digital Cross Connects (DXC), base stations, and central office switches."

About Cypress

Cypress Semiconductor provides high-performance integrated circuit solutions "By Engineers. For Engineers.™" for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial-control. With a focus on emerging communications applications, Cypress's product lines include networking-optimized and micropower static RAMs; high-bandwidth multiport and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). More than two-thirds of Cypress's sales come from fast-growing datacom/telecom markets and dynamic companies such as 3Com, Alcatel, Cisco, Ericsson, Lucent, Motorola, and Nortel Networks. Cypress is No. 1 in the USB and clock chip markets.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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